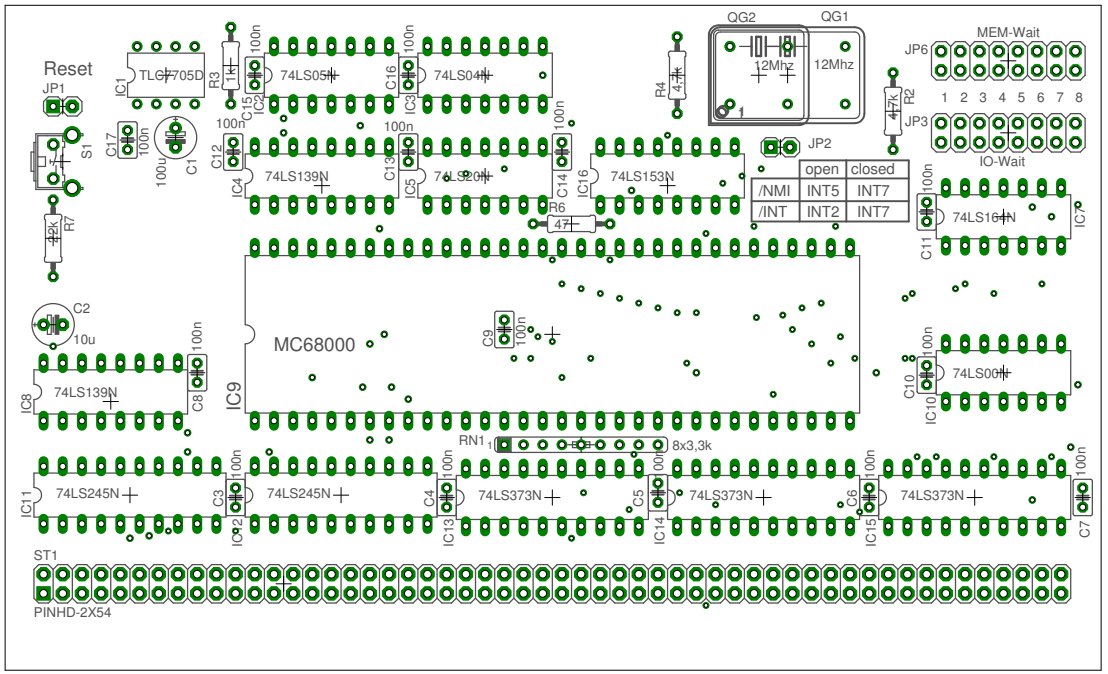
Reset
JP1
S1
IC1
TLC7705D
R3
1K
C15
100n
IC2
74LS05N
C16
IC3
74LS04N
C17
100n
100u
C1
C12
IC4
74LS139N
C13
IC5
74LS20N
C14
IC16
74LS153N
R6
47
R7
22K
C2
10u
IC8
74LS139N
C8
IC9
MC68000
C9
100n
RN1
8x3,3k
IC11
74LS245N
C3
IC12
74LS245N
C4
IC13
74LS373N
C5
IC14
74LS373N
C6
IC15
74LS373N
C7
ST1
PINHD-2X54
QG2
QG1
12Mhz
12Mhz
R4
4,7K
R2
47K
JP2
open
closed
/NMI
INT5
INT7
/INT
INT2
INT7
JP6
MEM-Wait
JP3
1 2 3 4 5 6 7 8
IO-Wait
C11
74LS161N
IC7
C10
74LS00N
IC10